

How would the cache change if we performed the following memory operations?

1. Read 01000100 (Value: 5)
2. Read 11100010 (Value: 17)
3. Write 01110000 (Value: 7)
4. Read 10101010 (Value: 12)
5. Write 01101100 (Value: 2)

Line	V	D	Tag	Data (4 Bytes)
0	1	0	111	17
1	1	0	011	9
2	0	0	101	15
3	1	1	001	8
4	1	0	011	4
5	0	0	111	6
6	0	0	101	32
7	1	0	110	3